



CENTRE FOR ELECTRONICS TEST ENGINEERING, PUNE

STQC Directorate, Department of Information Technology,
Ministry of Communications & Information Technology,
Government of India
(Institutionally Supported by MCCIA)



COURSE CALENDAR: APRIL 2010 – SEPTEMBER 2010

No	Course Title	Days	Fees (Rs)	April 2010	May 2010	June 2010	July 2010	August 2010	Sept. 2010
Area – Metrology & Instrumentation									
01	Calibration System Management	2	3600		11-12			11-12	
02	Calib. Tech for Elec. Para: DC, AC, L, C, R & Temperature & Pressure	3	5200				13-15		
03	Measurement Uncertainty	1	2500		13			13	
04	Fiber Optic Connectorisation & Splicing Techniques	2	4500	13 - 14			13 - 14		14 - 15
Area – Electronic Manufacturing Technology									
05	Certified IPC Specialist as per IPC A-610D	4	10000			21 - 24			27 - 30
06	Zero Defect Soldering	2	3000	8 - 9			20 - 21		
07	Workshop on Surface Mount Technology (SMT)	2	3000			15 - 16		17 - 18	
08	PCB Design	2	3000	6 - 7			27 - 28		21 - 22
09	Signal Integrity & high speed design at board level	2	5400			29 - 30		24 - 25	
10	ROHS implementation in Electronics Industry	1	2000			25		20	
Area – Industrial Automation & Networking									
11	Embedded System Design using Micro-controllers	5	6800						
12	P LC (Siemens S7-300)	5	8400						
Area – EMC									
13	CE Marking	1	2500		21				17
14	EMI/EMC Design Aspects	2	4500				6-7		
Area – Quality Management									
15	Measurement System Analysis	1	2000			17			9
16	Certified Calibration Professional	6	10500			7-12			
17	Internal Quality Audit	2	3600		13-14				29-30
18	Environmental Management System	2	3600						
19	Lead Auditor for ISO 9001: 2000	5	14000	26-30					
20	Laboratory Competence as per ISO/IEC 17025	2	3600		20-21			19-20	
Area – Others									
21	MS Quality Management: Contact session (BITS Pilani/IIQM Collaborative Programme)	12	-----				12-24		
22	Laboratory Management & Internal Audit (NABL Course)	4	7000						

- 10% discount for 3 or more nominations from the same organisation for the same course
- 10.3% Service Tax on basic course fees • TDS not applicable • Payment by DD only from any Nationalised bank in the name of "PAO, DIT" payable at Pune. At par cheque/Cash/Co-op. bank DD NOT accepted.
- **The dates are subject to change without any notice.** Please confirm the course dates before sending the nominations.
- For registration and further details, please contact::

Director,

Centre for Electronics Test Engineering

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